



F1007/2000P

09/847754

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 6,925,089 B2

Issued: August 2, 2005

Name of Patentee: Chow, et. al.

Assignee: Advanced Micro Devices, Inc..

Title of the Invention: MECHANISM TO CONSOLIDATE
HPNA THREE NETWORK STATES INTO TWO NETWORK
STATES

CERTIFICATION UNDER

37 C.F.R. § 1.8

I hereby certify that this correspondence (along with any item referred as being enclosed herewith) is being deposited with the U.S. Postal Service as first class mail with sufficient postage in an enveloped addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on March 24, 2006.


Toni Stanley

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FOR PTO'S MISTAKE (37 C.F.R. § 1.322(a))**

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Respectfully submitted,

WINSTEAD SECHREST & MINICK P.C.

Attorney for Assignee

By: 

Robert A. Voigt, Jr.
Reg. No. 47,159

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Dallas, Texas 75201
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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,925,089 B2
DATED : August 2, 2005
INVENTOR(S) : Peter Ka-Fai Chow, William Young

It is certified that error appears in the above-identified patent and that said Letters
Patent is hereby corrected as shown below:

Column 3, line 24, please create a new paragraph after "." and before "Link".

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